

EAST - (default.wsp.1)

File View Edit Tools Window Help

Drafts  
 Pending  
 Active  
 L1: (871) pulse and (multipulse or multi-pulse)  
 L2: (265) 1 and (correlation or convolution)  
 L3: (38) 1 and (correlation or convolution) and zeros  
 L4: (1) 1 and (correlation or convolution) same zeros  
 L5: (69) 1 and (correlation or convolution) same (zero or non-zero or nonzero)  
 L6: (17) mp-miq  
 L7: (2) (("6738733" or ("6687668")).PN.  
 L8: (4208) multipl\$ near2 zero  
 L9: (142) 8 and speech and pulse  
 L10: (248) multiply same accumulate same zero  
 L11: (70) multiply with accumulate with zero  
 L12: (3) 11 and speech and pulse  
 Failed  
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 UDC  
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Summary Copy Browse Queue Clear

DBs USPAT

Default operator OR

Highlight all hit terms initially

11 and speech and pulse

PDF form HTML form Image Text NUM

	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current Xref	Retrieval C	Inventor	B	C	P	US	6
1	<input type="checkbox"/>	<input type="checkbox"/>	US 6658578 B1	20031202	248	Microprocessors	713/324	712/244		Laurenti, Gilbert et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6
2	<input type="checkbox"/>	<input type="checkbox"/>	US 5983257 A	19991109	40	System for signal processing using multiply-add	708/603	708/620		Dulong, Carole et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5
3	<input type="checkbox"/>	<input type="checkbox"/>	US 5793661 A	19980811	46	Method and apparatus for performing multiply and	708/603			Dulong, Carole et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5

File Desktop HTML

Ready

NUM

Best Available Copy



**Best Available Copy**